

# PATENT ABSTRACTS OF JAPAN

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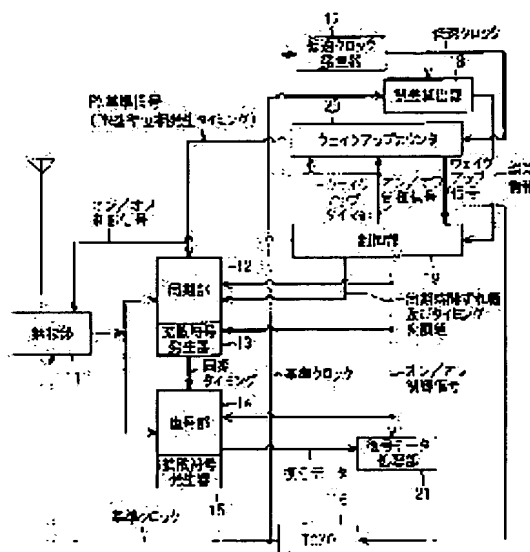
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## (54) RADIO COMMUNICATION APPARATUS

### (57)Abstract:

**PROBLEM TO BE SOLVED:** To use a low-speed clock and to reduce a power consumption in a reception waiting operation in a radio communication apparatus of a CDMA system.

**SOLUTION:** A low-speed clock generator 17, an error calculation part 18 and a wake-up counter 20 are used, and the low-speed clock is used and operated in a sleep operation. At this time, based on error information obtained in the error calculation part 18 by a control unit 19, the pilot synchronization time deviation width and the timing modulation value of a synchronization part 12 are set so as to be corrected by a slew control operation. The wake-up timing of a next reception slot is controlled with the frequency accuracy of a reference clock in a TCXO 16. As a result, even when the TCXO 16 is stopped during the sleep operation, a pilot search can be resynchronized and acquired without fail, and the power consumption in the reception waiting operation can be reduced.



## LEGAL STATUS

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